

**Amendments to the Specification:**

**The Paragraph beginning at Page 321, lines 9-15, is to be amended as follows:**

Fig. ~~¥37203~~ shows a schematic block diagram for the Counter Unit. The Counter Unit generates counters C1, C2 (used internally) and the selected N index. In addition, the Counter Unit outputs flags C1Z and C2Z for use externally. Registers C1 and C2 are updated when they are the targets of a DBR or SC instruction. The high bit of the operand (bit<sub>3</sub> of the effective command) gives the selection between C1 and C2. Logic<sub>1</sub> and Logic<sub>2</sub> determine the WriteEnables for C1 and C2 respectively.

**The Paragraph beginning at Page 345, lines 25-41, is to be deleted.**

**The Paragraphs beginning at Page 346, through to Page 347, ending at line 33 are to be deleted.**